

codesign virtual machine UML OR XML

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A codesign virtual machine for hierarchical, balanced hardware/software system modeling JAM Paul, SN Peffers, DE Thomas - PROC DES AUTOM CONF. pp. 390-395. 2000, 2000 - portal.acm.org ... a high system level to the machine implementation. ... Previous hardware-software codesign approaches do not ... unrestricted software and hardware virtual machines in ... Cited by 7 - Web Search - sslab.cs.nthu.edu.tw - ece.cmu.edu - sigda.org - all 11 versions »

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[PS] Heterogeneous Development of Hybrid Systems

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... Figure 3. The Visitor pattern in UML. ... version, for execution on a much smaller virtual
machine. ... Processors," in Hardware Software Codesign, Kluwer Academic ...
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Reengineering legacy embedded systems

VK Madisetti, YK Jung, MH Khan, J Kim, T Finnessy - IEEE Design & Test of Computers, 1999 - ieeexplore.ieee.org ... to develop an executable virtual prototype of the lega- cy system in a language such as VHDL (VHSIC 1-lardware Description Language) or UML (Unified Modeling ...

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	5867400[pn]	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/26 17:12
S2	1	10/074501	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/02 11:20
S3	242	matched with instruction with set	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/26 17:12
S4	12	matched adj2 instruction adj2 set	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR .	ON	2005/07/26 17:13
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S7	2	interconnected adj2 design adj2 component\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/26 17:15
S8	1	6122023[pn]	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 09:39
S9	8	dave\$[in] and @pd="20000829"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 10:06
S10	2	(US-6112023-\$ or US-6110220-\$). did.	USPAT	OR	ON	2005/07/27 09:40

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S11	363	co\$1design	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 10:06
S12	193	co\$1design and synthesis	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 10:07
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S16	171	co\$1design\$4 and synthe\$5 and compil\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 13:36
S19	1947	(703/13-16,20[ccls]).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/07/27 16:56
S20	3424	(716/5,6,11,2[ccls]).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/07/27 10:20
S21	3455	S20 or S19 and S16	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 10:20
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S27	2	(load with balancing) same (hardware with software) same reconfigure\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 10:37
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S29		(load with balancing) same (hardware with software) and reconfigure\$4 and @ad<"20010213"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 10:47
S30	695	718/105[ccls]	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 10:49
S32	230	718/105[ccor]	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 10:48
S33	85 _.	718/105[ccor] and (hardware with software) ,	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 10:49
S34	66	718/105[ccor] and (hardware adj3 software)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 10:49
S36	220	718/105[ccls] and (hardware adj3 software)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 10:49
S37	130	718/105[ccls] and (hardware adj3 software) and @ad<"20010213"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 11:05

S39	125	718/105[ccls] and (hardware adj3 software) and @ad<"20010213" and specif\$6	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 11:05
S40	98	718/105[ccls] and (hardware adj3 software) and @ad<"20010213" and design\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 11:12
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S43	16	718/105[ccls] and (hardware adj3 software) and @ad<"20010213" and design\$4 and re\$1configure\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 11:13
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S45	65	(718/105[ccls] and (hardware adj3 software) and @ad<"20010213" and design\$4) not "709"/\$[ccor]	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 11:14
S46	285	717/104[ccls]	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 13:36
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S50	354	717/108[ccls]	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 13:36
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S53	128	717/137[ccls]	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/07/27 13:38
S54	1947	(703/13-16,20).CCLS.	US-PGPUB; USPAT	OR ·	OFF	2005/07/27 16:56
S56	0	(UML XML) same codesign and (virtual with machine)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/02 11:21
S57	1	(UML XML) and codesign and (virtual with machine)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON .	2005/08/02 11:21
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